

*FlexROM*TM-Based 8-Bit CMOS Microcontroller Series

Devices Included in this Data Sheet:

- PIC16FR54A
- PIC16FR58A

High-Performance RISC CPU:

- Only 33 single word instructions to learn
- All instructions are single cycle (200 ns) except for program branches which are two-cycle
- Operating speed: DC - 20 MHz clock input
DC - 200 ns instruction cycle

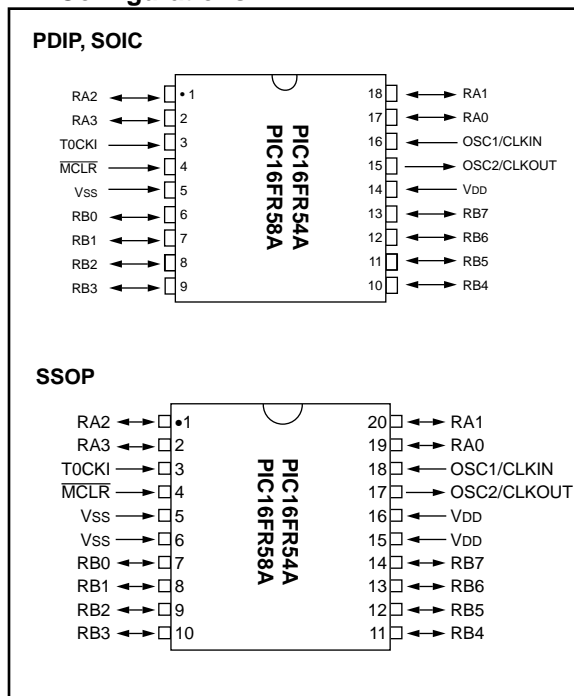
Device	Pins	I/O	FlexROM	RAM
PIC16FR54A	18	12	512	25
PIC16FR58A	18	12	2K	73

- 12-bit wide instructions
- 8-bit wide data path
- Seven special function hardware registers
- Two-level deep hardware stack
- Direct, indirect and relative addressing modes for data and instructions

Peripheral Features:

- 8-bit real time clock/counter (TMR0) with 8-bit programmable prescaler
- Power-On Reset (POR)
- Device Reset Timer (DRT)
- Watchdog Timer (WDT) with its own on-chip RC oscillator for reliable operation
- Selectable code-protection
- Power saving SLEEP mode
- Selectable oscillator options:
 - RC: Low-cost RC oscillator
 - XT: Standard crystal/resonator
 - HS: High-speed crystal/resonator
 - LP: Power saving, low frequency crystal

Pin Configurations



CMOS Technology:

- Low-power, high-speed CMOS *FlexROM* technology
- Fully static design
- Wide-operating voltage range:
 - *FlexROM* Commercial/Industrial 2.5V to 6.25V
- Low-power consumption
 - < 2 mA typical @ 5V, 4 MHz
 - 15 μ A typical @ 3V, 32 kHz
 - < 0.6 μ A typical standby current (with WDT disabled) @ 3V, 0°C to 70°C

Enhanced PIC16FR5X

1.0 GENERAL DESCRIPTION

The Enhanced PIC16FR5X from Microchip Technology is a family of low-cost, high performance, 8-bit, fully static, *FlexROM*-based CMOS microcontrollers. This family is pin and software compatible with the PIC16FR5X family of devices in a new enhanced process technology. It employs a RISC architecture with only 33 single word/single cycle instructions. All instructions are single cycle (200 ns) except for program branches which take two cycles. The Enhanced PIC16FR5X delivers performance an order of magnitude higher than its competitors in the same price category. The 12-bit wide instructions are highly symmetrical resulting in 2:1 code compression over other 8-bit microcontrollers in its class. The easy to use and easy to remember instruction set reduces development time significantly.

The Enhanced PIC16FR5X products are equipped with special features that reduce system cost and power requirements. The Power-On Reset (POR) and Device Reset Timer (DRT) eliminate the need for external reset circuitry. There are four oscillator configurations to choose from, including the power-saving LP (Low Power) oscillator and cost saving RC oscillator. Power saving SLEEP mode, Watchdog Timer and code protection features improve system cost, power and reliability.

The PIC16FR5X products are supported by a full-featured macro assembler, a software simulator, an in-circuit emulator, a 'C' compiler, fuzzy logic support tools. All the tools are supported on IBM[®] PC and compatible machines.

1.1 Applications

The PIC16FR5X series fits perfectly in applications ranging from high-speed automotive and appliance motor control to low-power remote transmitters/receivers, pointing devices and telecom processors. The *FlexROM* technology makes customizing application programs (transmitter codes, motor speeds, receiver frequencies, etc.) extremely fast and convenient. The small footprint packages, for through hole or surface mounting, make this microcontroller series perfect for applications with space limitations. Low-cost, low-power, high performance, ease of use and I/O flexibility make the PIC16FR5X series very versatile even in areas where no microcontroller use has been considered before (e.g., timer functions, replacement of "glue" logic in larger systems, coprocessor applications).

Enhanced PIC16FR5X

TABLE 1-1: ENHANCED PIC16FR5X FAMILY OF DEVICES

	Maximum Frequency of Operation (MHz)		FlexROM Program Memory (words)	RAM Data Memory (bytes)	Timer Module(s)	I/O Pins	Voltage Range (Volts)	Number of Instructions	Packages
	Clock	Memory	Peripherals	Features					
PIC16FR54A	20	512	25	TMR0	12	2.5-6.25	33	18-pin DIP, SOIC; 20-pin SSOP	
PIC16FR58A	20	2K	73	TMR0	12	2.5-6.25	33	18-pin DIP, SOIC; 20-pin SSOP	

Legend: All PIC16/17 Family devices have Power-On Reset, selectable Watchdog Timer, selectable code protect and high I/O current capability.

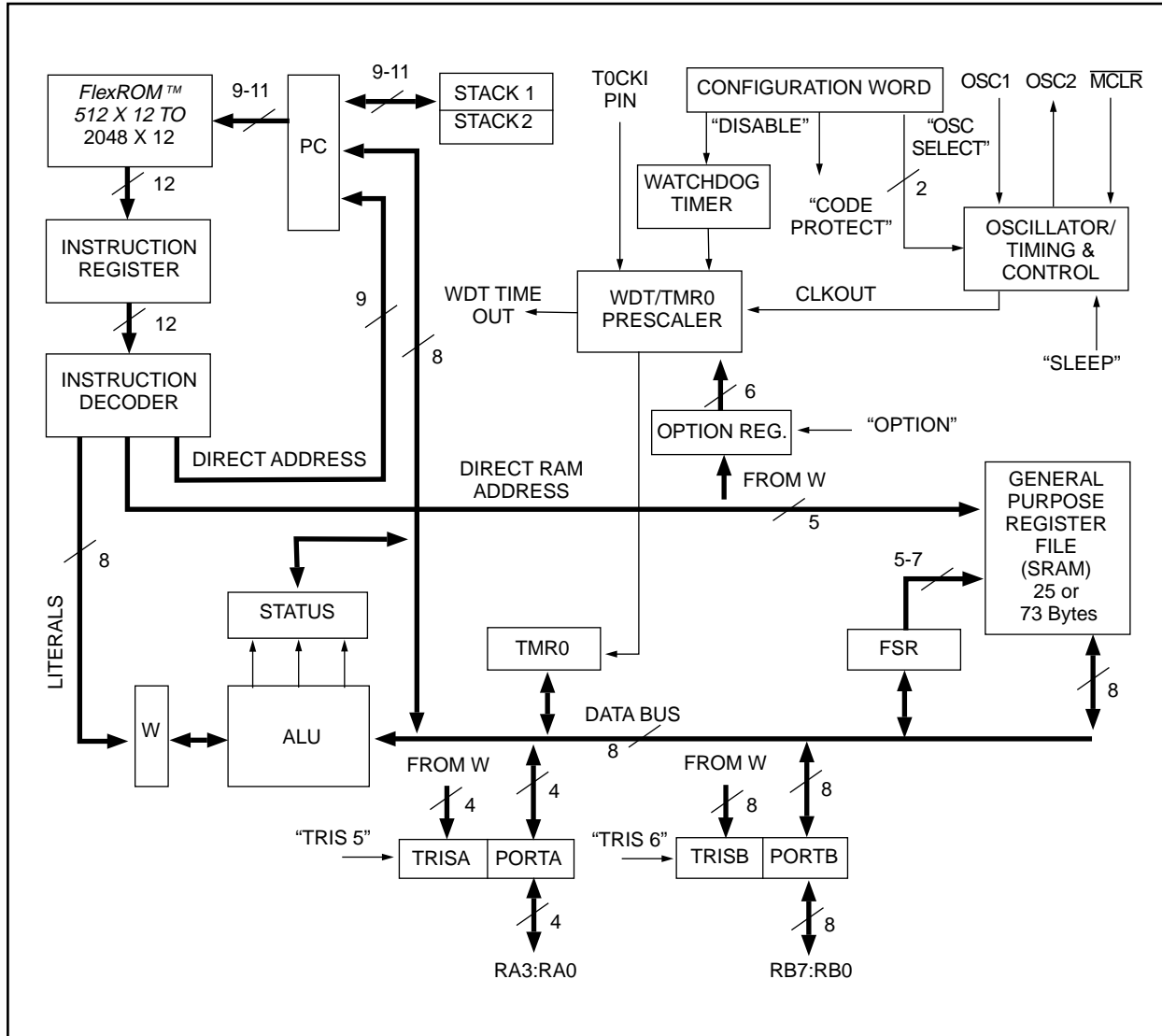
Note: Please contact your local sales office for availability of these devices.

Enhanced PIC16FR5X

2.0 ARCHITECTURAL OVERVIEW

This section provides information on the architecture of the Enhanced PIC16FR5X. For information on operation of the peripherals, electrical specifications, etc., please refer to the Enhanced PIC16C5X data sheet.

FIGURE 2-1: PIC16FR5X SERIES BLOCK DIAGRAM



Enhanced PIC16FR5X

TABLE 2-1: PINOUT DESCRIPTION - PIC16FR54A, PIC16FR58A

Name	DIP, SOIC No.	SSOP No.	I/O/P Type	Input Levels	Description
RA0	17	19	I/O	TTL	Bi-directional I/O port
RA1	18	20	I/O	TTL	
RA2	1	1	I/O	TTL	
RA3	2	2	I/O	TTL	
RB0	6	7	I/O	TTL	Bi-directional I/O port
RB1	7	8	I/O	TTL	
RB2	8	9	I/O	TTL	
RB3	9	10	I/O	TTL	
RB4	10	11	I/O	TTL	
RB5	11	12	I/O	TTL	
RB6	12	13	I/O	TTL	
RB7	13	14	I/O	TTL	
T0CKI	3	3	I	ST	Clock input to Timer0. Must be tied to Vss or VDD, if not in use, to reduce current consumption.
$\overline{\text{MCLR}}$	4	4	I	ST	Master clear (reset) input. This pin is an active low reset to the device.
OSC1/CLKIN	16	18	I	ST	Oscillator crystal input/external clock source input.
OSC2/CLKOUT	15	17	O	—	Oscillator crystal output. Connects to crystal or resonator in crystal oscillator mode. In RC mode, OSC2 pin outputs CLKOUT which has 1/4 the frequency of OSC1, and denotes the instruction cycle rate.
VDD	14	15,16	P	—	Positive supply for logic and I/O pins.
Vss	5	5,6	P	—	Ground reference for logic and I/O pins.

Legend: I = input, O = output, I/O = input/output,
P = power, — = Not Used, TTL = TTL input,
ST = Schmitt Trigger input

Enhanced PIC16FR5X

3.0 MEMORY ORGANIZATION

FIGURE 3-1: PIC16FR54A PROGRAM MEMORY MAP AND STACK

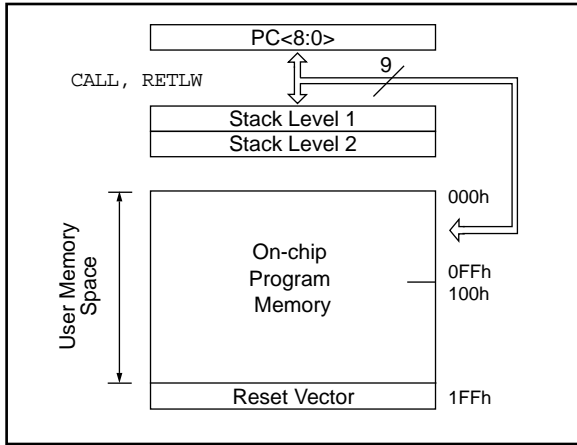


FIGURE 3-2: PIC16FR58A PROGRAM MEMORY MAP AND STACK

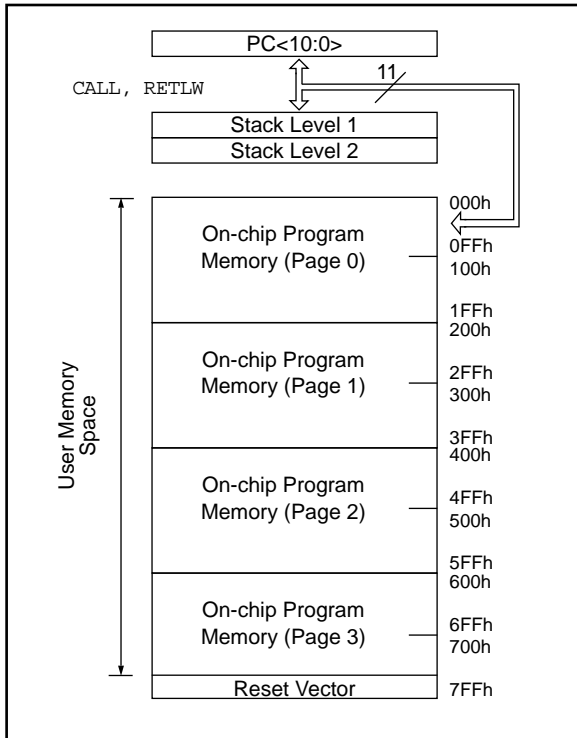


FIGURE 3-3: PIC16FR54A REGISTER FILE MAP

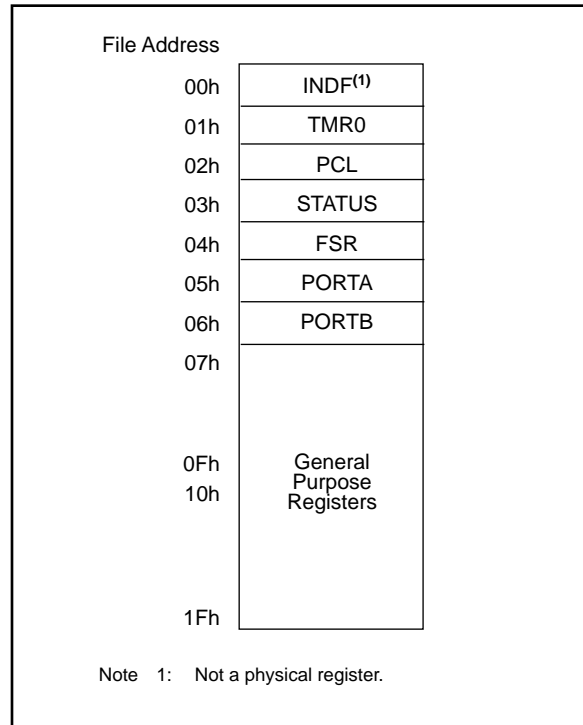


FIGURE 3-4: PIC16FR58A REGISTER FILE MAP

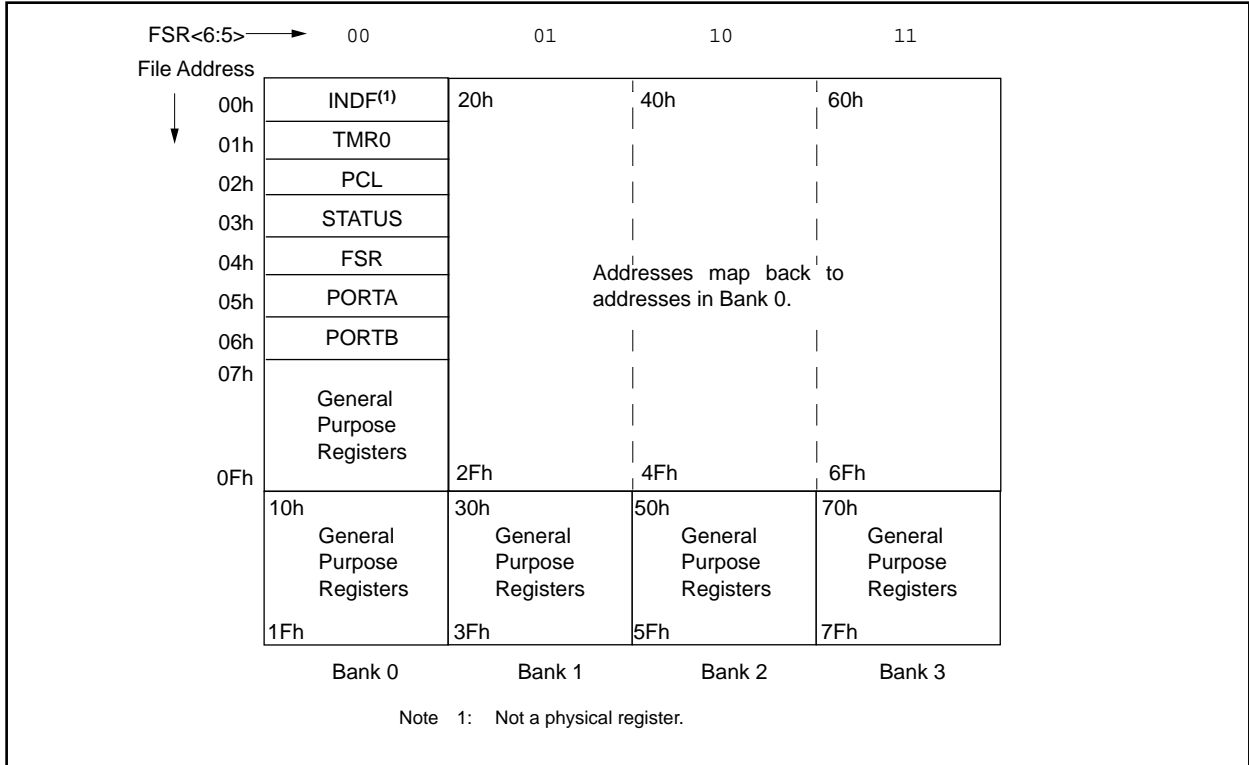


TABLE 3-1: SPECIAL FUNCTION REGISTER SUMMARY

Address	Name	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0	Value on Power-On Reset	Value on MCLR and WDT Reset
N/A	TRIS	I/O control registers (TRISA, TRISB, TRISC)								1111 1111	1111 1111
N/A	OPTION	Contains control bits to configure Timer0 and Timer0/WDT prescaler								--11 1111	--11 1111
00h	INDF	Uses contents of FSR to address data memory (not a physical register)								xxxx xxxx	uuuu uuuu
01h	TMR0	8-bit real-time clock/counter								xxxx xxxx	uuuu uuuu
02h ⁽¹⁾	PCL	Low order 8 bits of PC								1111 1111	1111 1111
03h	STATUS	PA2	PA1	PA0	\overline{TO}	\overline{PD}	Z	DC	C	0001 1xxx	000q quuu
04h	FSR	Indirect data memory address pointer								1xxx xxxx	1uuu uuuu
05h	PORTA	—	—	—	—	RA3	RA2	RA1	RA0	---- xxxx	---- uuuu
06h	PORTB	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0	xxxx xxxx	uuuu uuuu

Legend: Shaded boxes = unimplemented or unused, — = unimplemented, read as '0' (if applicable)
 x = unknown, u = unchanged, q = value depends on condition.

Note 1: The upper byte of the Program Counter is not directly accessible. See Section 4.5 of the Enhanced PIC16C5X data sheet (DS30236B) for an explanation of how to access these bits.

Note 2: File address 07h is a general purpose register on the PIC16FR54A and PIC16FR58A.

Enhanced PIC16FR5X

CONNECTING TO MICROCHIP BBS

Connect worldwide to the Microchip BBS using the CompuServe® communications network. In most cases a local call is your only expense. The Microchip BBS connection does not use CompuServe membership services, therefore, **you do not need CompuServe membership to join Microchip's BBS.**

There is **no charge** for connecting to the BBS, except toll charge to CompuServe access number, where applicable. You do not need to be a CompuServe member to take advantage of this connection (you never actually log in to CompuServe).

The procedure to connect will vary slightly from country to country. Please check with your local CompuServe agent for details if you have a problem. CompuServe service allows multiple users at baud rates up to 14,400 bps.

The following connect procedure applies in most locations:

1. Set your modem to 8-bit, No parity, and One stop (8N1). This is not the normal CompuServe setting which is 7E1.
2. Dial your local CompuServe access number.
3. Depress **<ENTER>** and a garbage string will appear because CompuServe is expecting a 7E1 setting.
4. Type **+**, depress **<ENTER>** and Host Name: will appear.
5. Type **MCHIPBBS**, depress **< ENTER>** and you will be connected to the Microchip BBS.

In the United States, to find CompuServe's phone number closest to you, set your modem to 7E1 and dial (800) 848-4480 for 300-2400 baud or (800) 331-7166 for 9600-14400 baud connection. After the system responds with Host Name:

Type, **NETWORK**, depress **< ENTER>** and follow CompuServe's directions.

For voice information (or calling from overseas), you may call (614) 723-1550 for your local CompuServe number.

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Device: **Enhanced PIC16FR5X** Literature Number: **DS40124A**

Questions:

1. What are the best features of this document? _____

2. How does this document meet your hardware and software development needs? _____

3. Do you find the organization of this data sheet easy to follow? If not, why? _____

4. What additions to the data sheet do you think would enhance the structure and subject? _____

5. What deletions from the data sheet could be made without affecting the overall usefulness? _____

6. Is there any incorrect or misleading information (what and where)? _____

7. How would you improve this document? _____

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Enhanced PIC16FR5X

ENHANCED PIC16FR5X PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.

<u>PART NO.</u>	<u>-XX</u>	<u>X</u>	<u>/XX</u>	<u>XXX</u>	
Device	Frequency Range	Temperature Range	Package	Pattern	
Device	PIC16FR5XA ⁽²⁾ , PIC16FR5XAT ⁽³⁾ PIC16LFR5XA ⁽²⁾ , PIC16LFR5XAT ⁽³⁾				Examples: a) PIC16FR54A -04/P 301 = Commercial temp., PDIP package, 4 MHz, normal VDD limits, pattern #301. b) PIC16LFR58A - 04I/SO123 = Industrial temp., SOIC package, 4 MHz, Extended VDD limits, pattern #123. Note 1: b = blank 2: FR = <i>FlexROM</i> Version, Standard VDD range LFR = <i>FlexROM</i> Version, Extended VDD range 3: T = in tape and reel - SOIC, SSOP packages only.
Frequency Range	04 = 4 MHz 10 = 10 MHz 20 = 20 MHz				
Temperature Range	b ⁽¹⁾ = 0°C to +70°C (Commercial) I = -40°C to +85°C (Industrial)				
Package	P = PDIP SO = SOIC (Gull Wing, 300 mil body) SS = SSOP (209 mil body)				
Pattern	3-digit Pattern Code for <i>FlexROM</i> (blank otherwise)				

Sales and Support

Products supported by a preliminary Data Sheet may possibly have an errata sheet describing minor operational differences and recommended workarounds. To determine if an errata sheet exists for a particular device, please contact one of the following:

1. Your local Microchip sales office (see below)
2. The Microchip Corporate Literature Center U.S. FAX: (602) 786-7277
3. The Microchip's Bulletin Board, via your local CompuServe number (CompuServe membership NOT required).

Please specify which device, revision of silicon and Data Sheet (include Literature #) you are using.

For latest version information and upgrade kits for Microchip Development Tools, please call 1-800-755-2345 or 1-602-786-7302.

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